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- DBs US-PGPUB: USPAT: USOCR: E ☐ Plurals
- Default operator: OR ☐ Highlight all hit terms initially

semiconductor and shallow adj trench adj isolation and  
anneal\$3 and (dielectric or insulating near film) and  
semiconductor near substrate and third near insulating near film

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20050230749 A1	20051020	14	Semiconductor device having a side wall insulating film and a manufacturing method thereof	257/335	
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20050226047 A1	20051013	38	Semiconductor device and method of manufacture thereof	365/185.14	
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20050224863 A1	20051013	29	Semiconductor device and method of manufacturing the same	257/315	
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20050196954 A1	20050908	39	Method for manufacturing semiconductor integrated circuit device	438/622	
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20050181605 A1	20050818	48	Method of manufacturing semiconductor device	438/673	
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20050156217 A1	20050721	26	Semiconductor memory device and method for fabricating the same	257/296	
7	<input type="checkbox"/>	<input type="checkbox"/>	US 20050127468 A1	20050616	15	Semiconductor device and method for manufacturing the same	257/501	257/446; 257/506; 438/210
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20050121788 A1	20050609	36	Semiconductor device	257/751	257/758; 257/762; 257/721, 576
9	<input type="checkbox"/>	<input type="checkbox"/>	US 20050121709 A1	20050609	71	Ferroelectric memory device and method of manufacturing the same	257/295	
10	<input type="checkbox"/>	<input type="checkbox"/>	US 20050116300 A1	20050602	30	Semiconductor device and method of fabricating the same	257/374	
11	<input type="checkbox"/>	<input type="checkbox"/>	US 20050067640 A1	20050331	66	Imaging device and manufacturing method thereof	257/291	